

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: DATE, HISAKAZU      Art Unit: 2138

Appln. No.: 10/807,446      Examiner:  
Radosevich

Filed: March 24, 2004      Conf. No.: 3358

For: SEMICONDUCTOR INTEGRATED CIRCUIT

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of June 7, 2006,  
please amend the above-identified patent application as  
indicated below.

Amendments to the claims are reflected in the listing  
of claims which begins on page 2 of this paper.

Amendments to the drawings begin on page 10 of this  
paper.

Remarks begin on page 11 of this paper.